TOSHIBA Transistor Silicon NPN Epitaxial Type

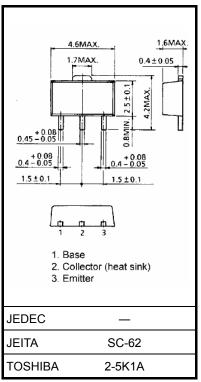
# 2SC6126

High-Speed Switching Applications DC-DC Converter Applications LCD Backlighting Applications

- High DC current gain:  $h_{FE}$  = 250 to 400 ( $I_C$  = 0.3 A)
- Low collector-emitter saturation: V<sub>CE(sat)</sub> = 0.18 V (max)
- High-speed switching: t<sub>f</sub> = 40 ns (typ.)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Collector-base voltage		V <sub>CBO</sub>	120	V	
Collector-emitter voltage		V <sub>CEX</sub>	120	V	
		V <sub>CEO</sub>	50	V	
Emitter-base voltage		V <sub>EBO</sub>	6	V	
Collector current (Note1)	DC	Ι <sub>C</sub>	3	А	
	Pulse	I <sub>CP</sub>	5	A	
Base current		Ι <sub>Β</sub>	1.5	А	
Collector power	DC	PC	1.0	W	
dissipation	t = 10 s	(Note2)	2.5		
Junction temperature		Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	–55 to 150	°C	



Weight: 0.05 g (typ.)

Note 1: Please use devices on condition that the junction temperature is below 150°C.

Note 2: Mounted on FR4 board (glass epoxy, 1.6 mm thick, Cu area: 645 mm<sup>2</sup>)

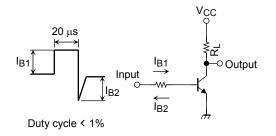
Note 3: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

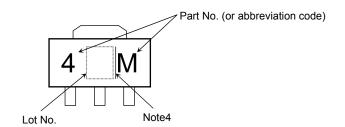
Electrical Characteristics (Ta = 25°C)

Characteristic		Symbol	Test Condition	Min	Тур.	Мах	Unit
Collector cutoff current		I <sub>CBO</sub>	$V_{CB} = 120 V, I_E = 0$	_	_	100	nA
Emitter cutoff current		I <sub>EBO</sub>	$V_{EB} = 6 V, I_{C} = 0$	1		100	nA
Collector-emitter bi	reakdown voltage	V (BR) CEO	$I_{C} = 10 \text{ mA}, I_{B} = 0$	50	_	—	V
DC current gain		h <sub>FE</sub> (1)	$V_{CE} = 2 V, I_C = 0.3 A$	250	_	400	-
		h <sub>FE</sub> (2)	$V_{CE} = 2 V, I_C = 1.0 A$	100	_	_	
Collector emitter sa	aturation voltage	V <sub>CE (sat)</sub>	I <sub>C</sub> = 1.0 A, I <sub>B</sub> = 33 mA	_	_	0.18	V
Base-emitter saturation voltage		V <sub>BE (sat)</sub>	I <sub>C</sub> = 1.0 A, I <sub>B</sub> = 33 mA	_	_	1.1	V
Collector output capacitance		Cob	V <sub>CB</sub> = 10 V, I <sub>E</sub> =0 ,f=1 MHz	_	10.5	_	pF
Switching time	Rise time	tr	See Figure 1 circuit diagram $V_{CC} \simeq 20 \text{ V}, \text{ R}_L = 20 \Omega$ I <sub>B1</sub> = 33 mA I <sub>B2</sub> = 33 mA	_	30		
	Storage time	t <sub>stg</sub>		_	500		ns
	Fall time	t <sub>f</sub>			40		

### Figure 1. Switching Time Test Circuit & Timing Chart



#### Marking

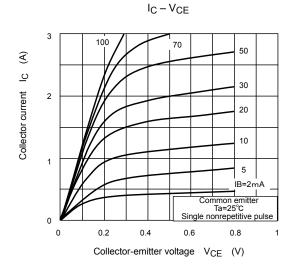


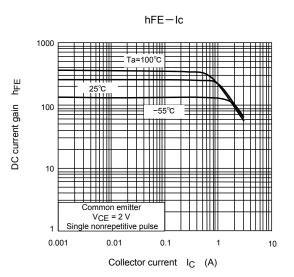
Note4 :A line to the right of a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

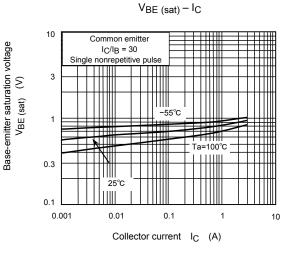
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

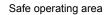
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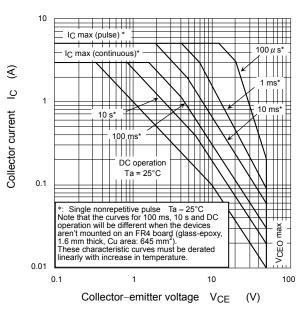
## TOSHIBA



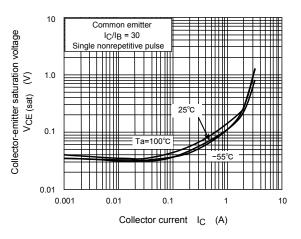


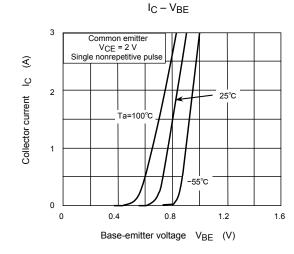


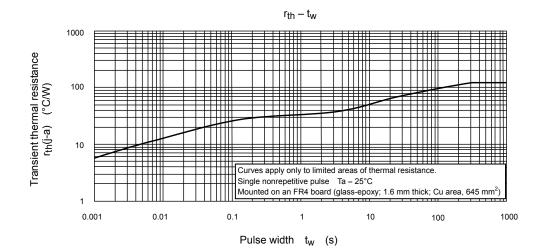




V<sub>CE (sat)</sub> – I<sub>C</sub>







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